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PIPE JC174  
FEB 05 2003  
PATENT & TRADEMARK OFFICE

## TECHNOLOGY CENTER R3700

FEB 10 2003

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Applicant:	Sung Gue LEE et al.	Conf.:	6825
Appl. No.:	10/043,146	Group:	Unassigned
Filed:	January 14, 2002	Examiner:	UNASSIGNED
For:	HOLE PLUGGING METHOD FOR PRINTED CIRCUIT BOARDS, AND HOLE PLUGGING DEVICE		

## LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents  
Washington, DC 20231

**FEB 05 2003**

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	38	-	34	=	4	\$18	\$72.00
INDEPENDENT	9	-	7	=	2	\$84	\$168.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$240.00

Appl. No. 10/043,143

- ☐ Petition for ( ) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☐ No fee is required.
- ☒ Check(s) in the amount of \$240.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By James T. Eller, Jr.  
James T. Eller, Jr. #39,538

JTE:sld  
0630-1402P

P.O. Box 747  
Falls Church, VA 22040-0747  
(703) 205-8000

Attachment(s)

(Rev. 10/15/02)



PATENT  
0630-14022

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Sung Gue LEE et al. Conf.: 6825  
Appl. No.: 10/043,146 Group: Unassigned  
Filed: January 14, 2002 Examiner: Unassigned  
For: HOLE PLUGGING METHOD FOR PRINTED CIRCUIT  
BOARDS, AND HOLE PLUGGING DEVICE

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, DC 20231

FEB 05 2003

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE CLAIMS:

Please add the following claims:

--35. A plugging method for a printed circuit board having a plurality of first circuit patterns formed on a surface of the board, a plurality of second circuit patterns formed in the board, comprising the steps of:

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02 FC:1202

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